S22 THRU **S2A0**

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIERS

FEATURES:

- Plastic package has Underwriters Laboratory
 Flammability Classification 94V-0
- For surface mounted applications
- Low profile package
- Built-in strain relief
- Metal silicon junction, majority carrier conduction
- Low power loss, high efficiency
- High current capability, low forward voltage drop
- High surge capability
- For use in low voltage high frequency inverters, free wheeling, and polarity protection applications
- Guard ring for overvoltage protection
- High temperature soldering quaranteed:
 250 ℃ /10 seconds at terminals

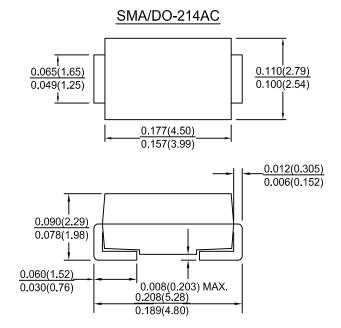
MECHANICAL DATA

Case: JEDEC DO-214AC molded plastic body
Terminals: Solder plated, solderable per MIL-STD-750

Method 2026

Polarity: Color band on body denotes cathode end

Weight: 0.002 ounce, 0.064 grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at $25\,^{\circ}\mathrm{C}$ ambient temp. unless otherwise specified. Single phase, half sine wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20 %.

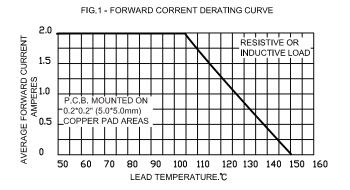
Characteristic	Symbol	S22	S23	S24	S25	S26	S28	S29	S2A0	Units
Maximum recurrent peak reverse voltage	VRRM	20	30	40	50	60	80	90	100	Volts
Maximum RMS voltage	VRMS	14	21	28	35	42	56	64	70	Volts
Maximum DC blocking voltage	VDC	20	30	40	50	60	80	90	100	Volts
Maximum average forward rectified current at TL(SEE FIG.1) TL=105 ℃	I _(AV)	2.0								Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load(JEDEC Method)	IFSM	50.0							Amps	
Maximum instantaneous forward voltage at 2.0 A(NOTE 1)	VF	0.50 0.70 0.85						Volts		
Maximum instantaneous reverse current at rated DC blocking voltage (NOTE 1)Ta=25 ℃ Ta=100 ℃	IR	0.5 20.0							mA	
Maximum thermal resistance(NOTE 2)	Rth-JA Rth-JL		55.0 17.0							°C/W
Operating junction temperature range	TJ	-65 to +150							${\mathbb C}$	
Storage temperature range	Tstg	-65 to +150							${\mathbb C}$	

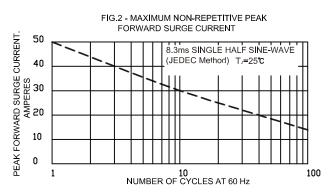
NOTES

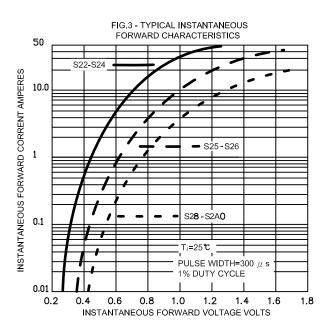
- (1) Pulse test: 300 μ s pulse width,1% duty cycle
- (2) P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas

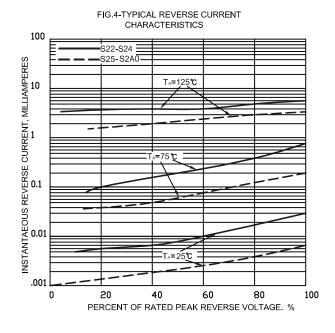
S22 THRU S2A0

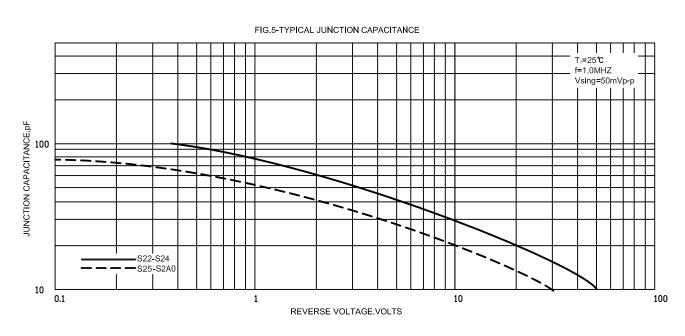
RATINGS AND CHARACTERISTIC CURVES













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