## SCHOTTKY BARRIER RECTIFIER

## FEATURES:

- Plastlc package Underwriters Laboratory Flammabllity Classiflcation 94VO
- Dual rectifler construction, positive centertap
- Metal sillcon Junctlon Majorlty carrier conduction
- Low power loss,high efficlency
- HIgh current capabllity, low forward voltage drop
- High temperature solderIngguaranteed:
$250^{\circ} \mathrm{C} / 10$ seconds, $0.25^{\prime}(6.35 \mathrm{~mm})$ from case
MECHANICAL DATA
Case : JEDEC TO-220AB molded plastlc
Terminals : Leads solderable per MIL-STD-750
Method 2026
Polarlty : As marked
Mounting Positlon: Any
Mounting Torque5 In - lbs.max
Welght : 0.08 ounce, 2.24 grams


Dimensions in inches and (millimeters)

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at $25^{\circ} \mathrm{C}$ ambient temperature unless otherwise specified.
Single phase half wave, 60 Hz resistive or inductive load.
For capacitive load. derate current by $20 \%$.

| Characteristic | Symbol | SR10120CT | Units |
| :---: | :---: | :---: | :---: |
| Maximum recurrent peak reverse voltage | VRRM | 120 | Volts |
| Maximum RMS voltage | VRMS | 85 | Volts |
| Maximum DC blocking voltage | VDC | 120 | Volts |
| Maximum average forward rectified current at Tc $=90^{\circ}$ (Per Pak) | $l_{\text {(AV) }}$ | 10 | Amps |
| Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)(Per leg) | IFSM | 80 | Amps |
| Maximum instantaneous forward voltage  <br> (Per leg)(NOTE 2) IF $=5 \mathrm{~A}$ | $V_{F}$ | 0.90 | Volts |
| Maximum instantaneous reverse current at rated DC blocking $\mathrm{Tc}=25^{\circ} \mathrm{C}$ voltage(Per leg)(NOTE 2) <br> TC $=125^{\circ} \mathrm{C}$ <br> volage(Perleg)(NOTE 2) | IR | $\begin{array}{r} 0.5 \\ 35.0 \end{array}$ | mA |
| Typical thermal resistance(Per leg)(NOTE 1) | $\mathrm{R}_{\text {th }}-\mathrm{JC}$ | 5.0 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating temperature range | $T_{J}$ | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range | TStg | $-65 t o+150$ | ${ }^{\circ} \mathrm{C}$ |

NOTES:
(1)Thermal resistance from junction to case
(2)Pulse test: 300 us pulse width, $1 \%$ duty cycle
(3)Marking : $\frac{\text { SR10120CT }}{\text { Symbol }}=\frac{\text { SR10120 }}{\text { Marking }}$ (Without Marking "CT")

Symbol Marking

RATINGS AND CHARACTERISTIC CURVES


FIG. 3 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT


FIG.4- TYPICAL JUNCTION CAPACITANCE



FIG.5- TYPICAL REVERSE CHARACTERISTICS


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