



SCHOTTKY BARRIER RECTIFIERS

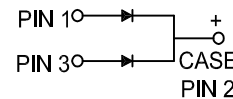
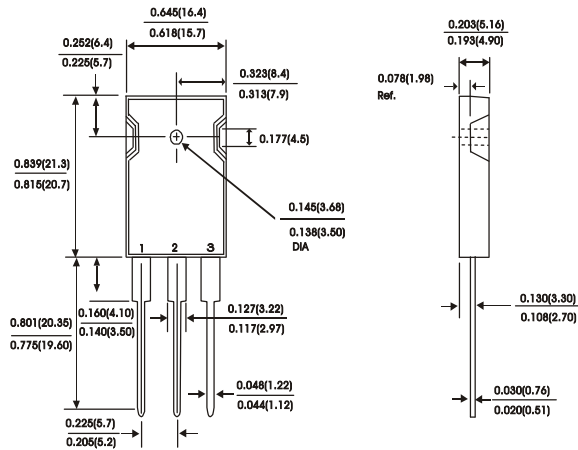
TO-247AD/TO-3P

FEATURES:

- Plastic package Underwriters Laboratory Flammability Classification 94V-0
- Dual rectifier construction, positive center tap
- Metal silicon junction Majority carrier conduction
- Low power loss, high efficiency
- High current capability, low forward voltage drop
- High temperature soldering guaranteed: 250°C/10 seconds, 0.25" (6.35mm) from case

MECHANICAL DATA

- Case : JEDEC TO-3P molded plastic
- Terminals : Leads solderable per MIL-STD-750 Method 2026
- Polarity : As marked
- Mounting Position : Any
- Mounting Torque 10 In - lbs. max
- Weight : 0.20 ounce, 5.6 grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25° C ambient temperature unless otherwise specified.
 Single phase half wave, 60 Hz resistive or inductive load.
 For capacitive load, derate current by 20%.

Characteristic	Symbol	SR 3020PT	SR 3030PT	SR 3035PT	SR 3040PT	SR 3045PT	SR 3050PT	SR 3060PT	Units
Maximum recurrent peak reverse voltage	V_{RRM}	20	30	35	40	45	50	60	Volts
Maximum RMS voltage	V_{RMS}	14	21	25	28	32	35	42	Volts
Maximum DC blocking voltage	V_{DC}	20	30	35	40	45	50	60	Volts
Maximum average forward rectified current at See fig.1	I_O	30							Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)(Per leg)	I_{FSM}	200							Amps
Maximum instantaneous forward voltage (Per leg)(NOTE 2) $I_F = 15A$	V_F	0.63					0.70		Volts
Maximum instantaneous reverse current at rated DC blocking voltage (Per leg)(NOTE 2) $T_c = 25^\circ C$ $T_c = 125^\circ C$	I_R	1.0 60.0					5.0 100.0		mA
Typical thermal resistance(Per leg)(NOTE 1)	R_{th-JC}	1.4							°C/W
Operating temperature range	T_J	-65to+150							°C
Storage temperature range	T_{Stg}	-65to+175							°C

NOTES:

- (1) Thermal resistance from junction to case
- (2) Pulse test : 300 us pulse width, 1% duty cycle



RATINGS AND CHARACTERISTIC CURVES

FIG.1 - TYPICAL FORWARD CORRENT DERATING CURVE

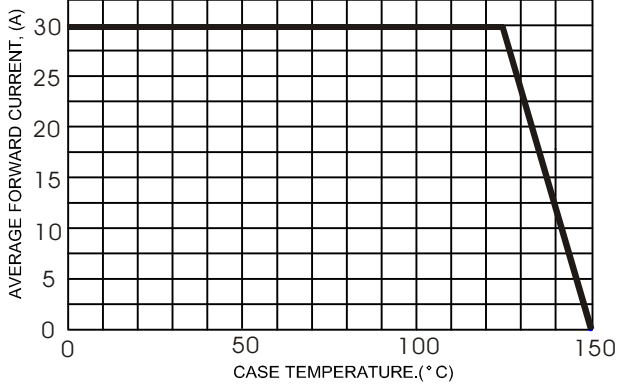


FIG.2 - TYPICAL FORWARD CHARACTERISTICS

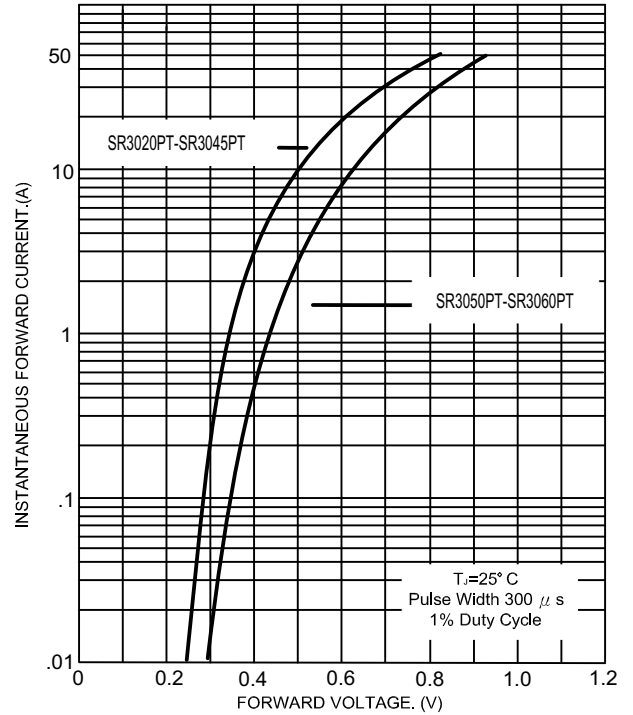


FIG.3 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

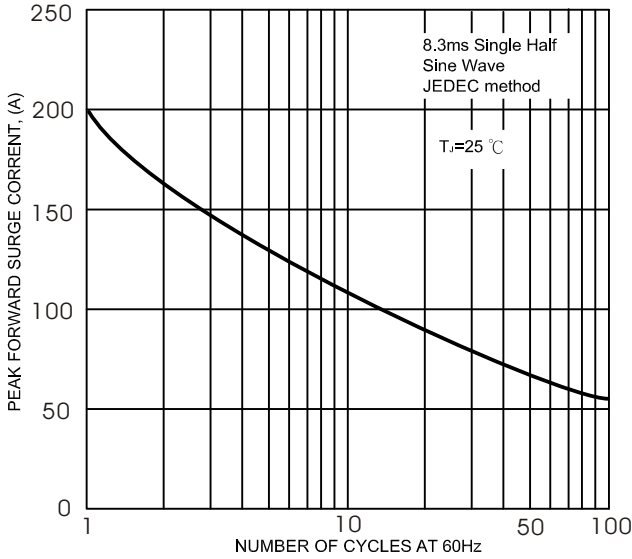


FIG.5- TYPICAL REVERSE CHARACTERISTICS

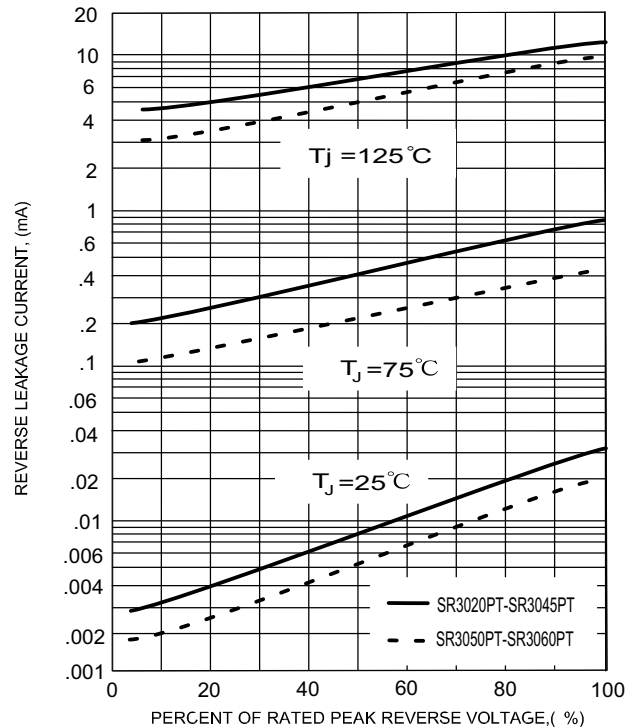
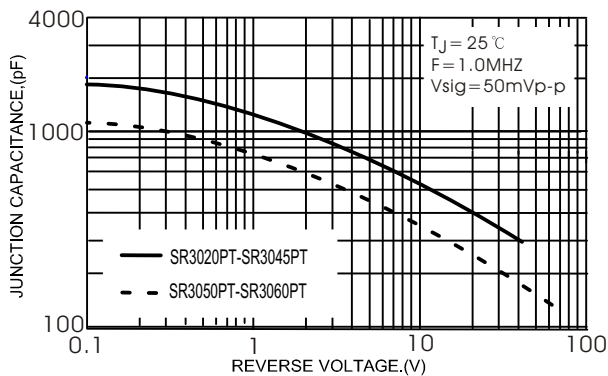


FIG.4- TYPICAL JUNCTION CAPACITANCE





Disclaimer

DACO Semiconductor reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein.

DACO Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does DACO Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages.

Purchasers is responsible for its products and applications using DACO Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by DACO Semiconductor. "Typical" parameters which may be provided in DACO Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts.

DACO Semiconductor products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of DACO Semiconductor's product can reasonably be expected to result in personal injury, death or severe property or environmental damage. DACO Semiconductor accept no liability for inclusion and/or use of DACO Semiconductor's products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Purchasers buy or use DACO Semiconductor products for any such unintended or unauthorized application, Purchasers shall indemnify and hold DACO Semiconductor and its suppliers and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that DACO Semiconductor was negligent regarding the design or manufacture of the part.

No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of DACO Semiconductor Co., Ltd.