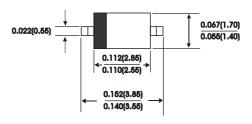
SD103AW/BW/CW

SMALL SIGNAL SCHOTTKY DIODES

SOD-123

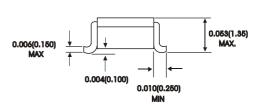
Features:

- *Low Forward Voltage
- *Very Small Conduction Losses
- *Schottky Barrier Diodes Encapsulated in a SOD-123 Package



Mechanical Data:

- *Polarity: Cathode Band
- *Leads: Solderable per MIL-STD-202 Method 208
- *Wight: 0.01grams(approx)



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25° C ambient temperature unless otherwise specified.

Single phase half wave, 60 Hz resistive or inductive load.

For capacitive load, derate current by 20%.

Maximum Ratings (TA=25°C Unless otherwise noted)

Characteristic	Symbol	SD103AW	SD103BW	SD103CW	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _R WM	40	30	20	٧
RMS Reverse Voltage	V _{R(RMS)}	28	21	14	V
Average Rectified Output Current	I _{FAV}	350			mA
Non-Repetitive Peak Forward Surge Current @t≦1.0S	lfsm	1.5		А	
Power Dissipation (1)	P _D	400		mW	
Typical thermal Resistance junction to Ambient Note (1)	R _{ÐJA}	300			°C/W
Operating & Storage Temperature Range	TJ TSTG	-55 to +125			°C

RATINGS AND CHARACTERISTIC CURVES

Electrical Characteristics (TA=25°C Unless otherwise noted)

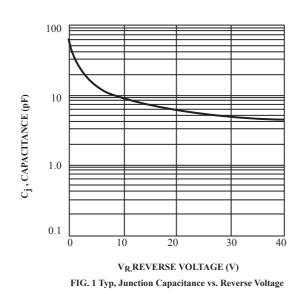
Characteristic	;	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (2)	SD103AW SD103BW SD103CW	V _{(BR)R}	40 30 20	- - -	- - -	V
Forward Voltage Note ⁽²⁾ IF=20mA IF=200mA		VF		1 1	0.37 0.60	V
Reverse Current Note ⁽²⁾	V _R =30V, SD103AW V _R =20V, SD103BW V _R =10V, SD103CW	IR	1	1	5.0	μΑ
Junction Capacitance, f=1MHZ,	V _R =0VDC	Cj		50		PF
Reverse Recovery Time I _F =	=I _F =200mA, =0.1*I _R ,R _L =100Ω	t _{rr}		10		ns

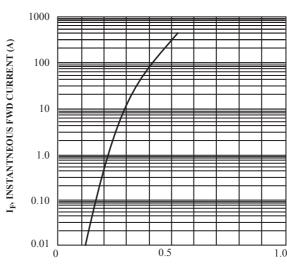
Device Marking

Item	Marking		Eqivalent Circuit diagram	
SD103AW	S4			
SD103BW	S5	S4	1 ○	
SD103CW	S6			

Note: 1. Valid provited that leads are kept at ambient tememperature.

2. Pulse Test: Pulse width = 300us, Duty Cycle ≤2%





VF, INSTANTANEOUS FORWARD VOLTAGE (V) FIG. 2 Typical Forward Characterictics

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SD103AW/BW/CW

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