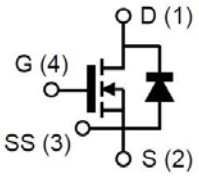
DAC026N170ZY3

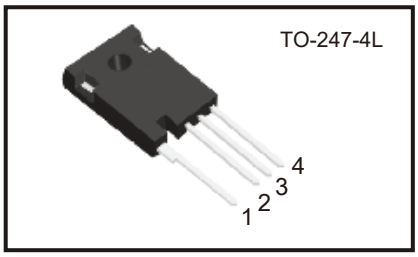
Silicon Carbide Enhancement Mode MOSFET

Features

- High blocking voltage with low Rds(on)
- High frequency operation with low Capacitance
- Simple to drive with -5V/+18V gate
- Robust body diode with low Qrr
- 100% Avalanche Tested

V_{DSS}	1700V
$I_{D(@25℃)}$	90A
$R_{DS(ON)\ typ.}$	26mΩ





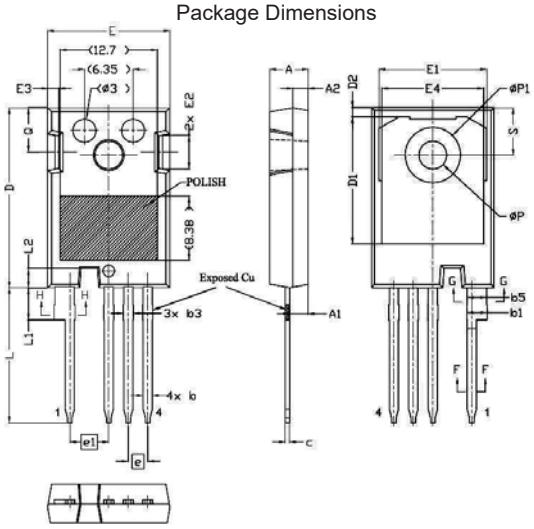
Benefits

- Superior robustness and system reliability
- Higher system efficiency
- Easier paralleling without thermal runaway
- Capable of high temperature application
- Faster and more efficient switching

Applications

- EV motor drives
- EV/HEV charging station
- Energy storage and Battery charging
- High voltage DC-DC converters
- Solar / Wind Inverters
- UPS and PFC

Package Dimensions



SYMBOL	DIMENSIONS		
	MIN.	NOM.	MAX.
A	4.83	5.02	5.21
A1	2.29	2.41	2.54
A2	1.91	2.00	2.16
b'	1.07	1.20	1.28
b	1.07	1.20	1.33
b1	2.39	2.67	2.94
b2	2.39	2.67	2.84
b3	1.07	1.30	1.60
b4	1.07	1.30	1.50
b5	2.39	2.53	2.69
b6	2.39	2.53	2.64
c	0.55	0.60	0.68
c1	0.55	0.60	0.65
D	23.30	23.45	23.60
D1	16.25	16.55	17.65
D2	0.95	1.19	1.25

SYMBOL	DIMENSIONS		
	MIN.	NOM.	MAX.
E	15.75	15.94	16.13
E1	13.10	14.02	14.15
E2	3.68	4.40	5.10
E3	1.00	1.45	1.90
E4	12.38	13.26	13.43
e	2.54 BSC		
e1	5.08 BSC		
L	17.31	17.57	17.82
L1	3.97	4.19	4.37
L2	2.35	2.50	2.65
ØP	3.51	3.61	3.65
ØP1	7.19 REF.		
Q	5.49	5.79	6.00
S	6.04	6.17	6.30

Absolute Maximum Ratings

(Tc = 25℃ unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage	V_{GS}=0V I_D=100μA	V_{DS}	1700	V
Gate-Source Voltage (dynamic)	AC (f>1 Hz, duty cycle<1%, pulse width<200ns)	V_{GS}	-9/+22	V
Gate-Source Voltage (static)		$V_{GS(op)}$	-5/+18	V
Drain Current-Continuous	V_{GS}=18V@ T_C=25°C V_{GS}=18V@ T_C=100°C	I_D	90 65	A
Pulse Drain Current		$I_{D,pulse}$	278	A
Power Dissipation		P_D	535	W
Storage Temperature Range		T_{STG}	-55 to +175	°C
Operating Junction Temperature Range		T_J	-55 to +175	°C
Soldering Temperature		T_L	260	°C
Avalanche Capability, single pulse *	V_{DD}=100V V_{GS}=10V L=2mH	I_{AV}	50	A
Avalanche Capability, single pulse**	V_{DD}=100V V_{GS}=10V L=2mH	E_{AV}	2400	mJ

* 100% tested in 60% rating
** 100% tested in 36% rating

Electrical Characteristics @ T_C =25°C (unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
OFF Characteristics							
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} =0V，I_D =0.1mA		1700	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =1700V V_{GS} =0V	T_J =25℃	-	0.5	100	μA
			T_J =175℃	-	1	-	
Gate-Source Leakage Current	I_{GSS}	V_{GS} =18V，V_{DS} =0V		-	5	100	nA
		V_{GS} =-5V，V_{DS} =0V		-100	-5	-	
ON Characteristics							
Gate Threshold Voltage ***	$V_{GS(th)}$	V_{DS} = V_{GS}，I_D =20mA	T_J =25℃	2.4	3.1	4.2	V
			T_J =175℃	-	2.2	-	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS} =18V，I_D =60A	T_J =25℃	-	26	33	mΩ
			T_J =175℃	-	64	-	
Transconductance	g_{fs}	V_{DS} =20V，I_D =60A	T_J =25℃	-	52	-	S
			T_J =175℃	-	46	-	
Internal Gate Resistance	$R_{G(int.)}$	f =1MHz，I_D =0A		-	1.3	-	Ω
Dynamic Characteristics							
Input Capacitance	C_{iss}	V_{DS} =1200V V_{GS} =0V f =100kHz V_{AC} =25mV		-	4400	-	pF
Output Capacitance	C_{oss}			-	120	-	
Reverse Transfer Capacitance	C_{rss}			-	12	-	
C oss Stored Energy	E_{oss}			-	105	-	μJ
Turn-On Switching Energy	E_{on}	V_{DS} =1200V，V_{GS} =-5/+18V I_D =60A，$R_{G(ext)}$ =2.0Ω L=200μH		-	850	-	μJ
Turn-Off Switching Energy	E_{off}			-	230	-	
Switching Characteristics							
Turn-On Delay Time	$t_{d(on)}$	V_{DS} =1200V，V_{GS} =-5/+18V I_D =60A，$R_{G(ext)}$ =2.0Ω L=200μH		-	18	-	ns
Rise Time	t_r			-	21	-	
Turn-Off Delay Time	$t_{d(off)}$			-	44	-	
Fall Time	t_f			-	11	-	
Total Gate Charge	Q_g	V_{DS} =1200V V_{GS} =-5/+18V I_D =60A		-	230	-	nC
Gate to Source Charge	Q_{gs}			-	68	-	
Gate to Drain Charge	Q_{gd}			-	90	-	
Body Diode Characteristics							
Inverse Diode Forward Voltage	V_{SD}	V_{GS}=-5V，I_{SD}=40A	T_J =25℃	-	4.4	-	V
Inverse Diode Forward Voltage			T_J =175℃	-	3.8	-	V
Continuous Diode Forward Current	I_S	V_{GS}=-5V，T_J =25°C		-	90	-	A
Reverse Recovery Time	T_{rr}	I_{SD}=60A，V_{GS}=-5V V_R=1200V dif/dt=1780A/μs		-	30	-	ns
Reverse Recovery Charge	Q_{rr}			-	550	-	nC
Reverse Recovery Charge	I_{rrm}			-	36	-	A
Thermal Resistance							
Thermal Resistance, Junction-to-Case	$R\theta_{JC}$			-	0.25	0.28	°C/W

*** Turn-off with -5V gate bias is highly recommended

Typical Performance

Fig 1. Output Characteristics, T_J = 25°C

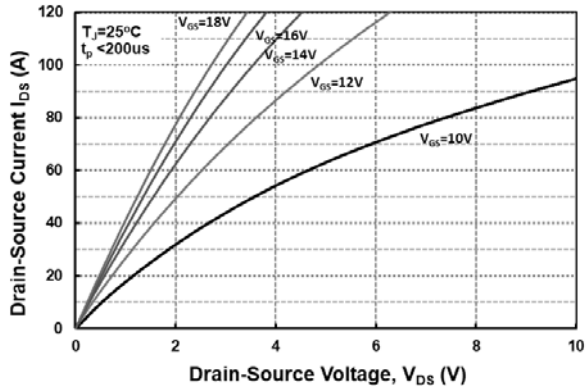


Fig 2. Output Characteristics, T_J = 175°C

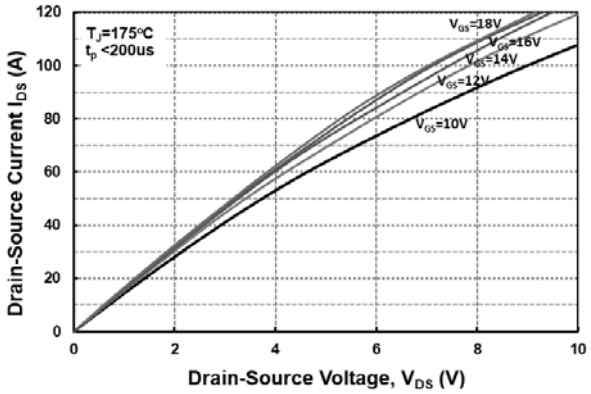


Fig 3. On-Resistance vs. Drain Current for Various Temperatures

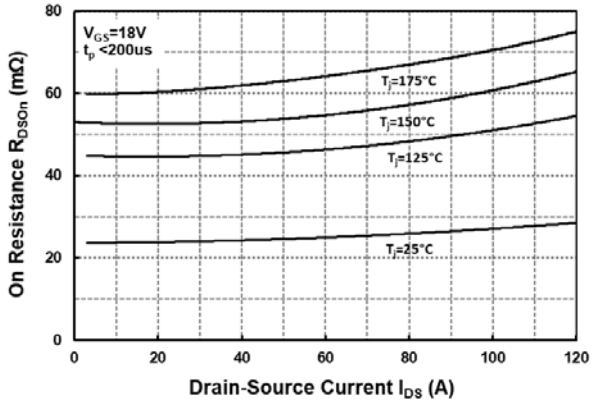


Fig 4. On-Resistance vs. Temperature for Various Gate Voltage

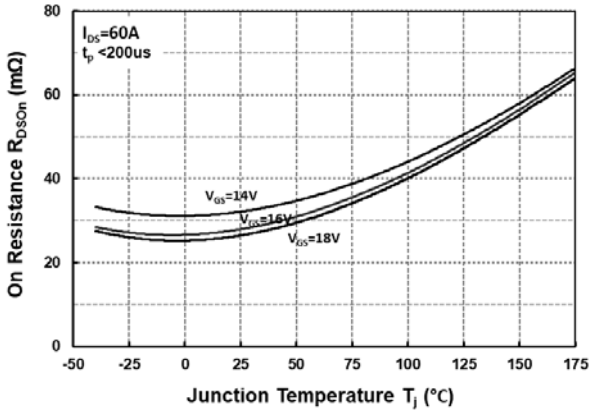


Fig 5. Output Characteristics vs temp, V_{GS} = 18V

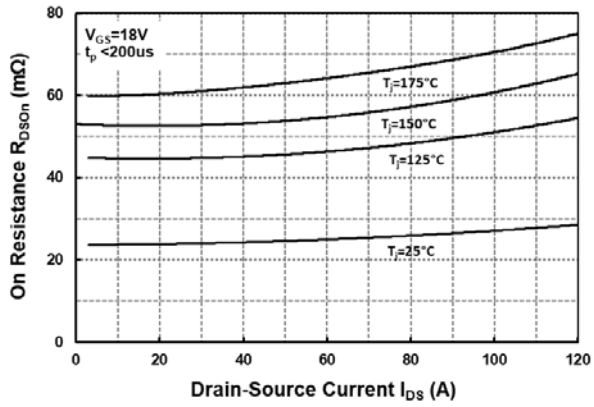
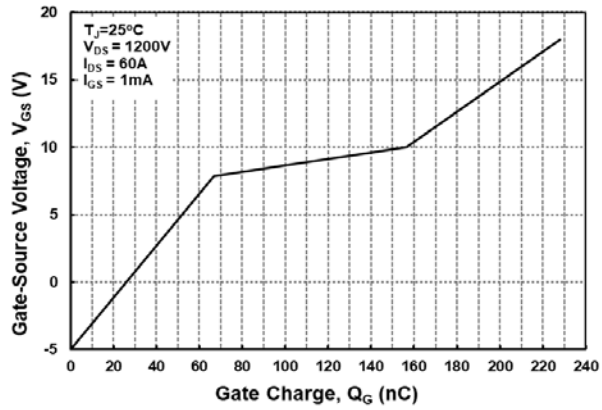


Fig 6. Gate Charge Characteristics



Typical Performance

Fig 7. Transfer Characteristic for Various Junction Temperatures

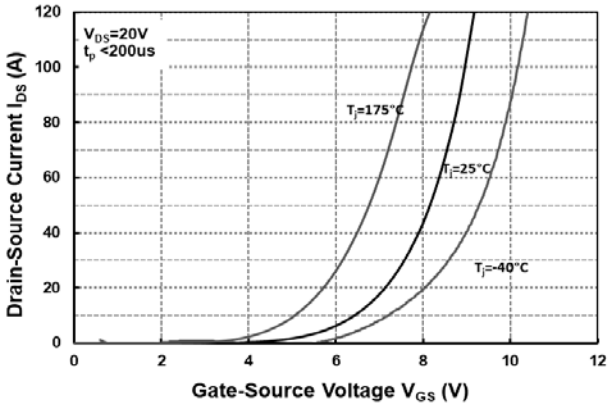


Fig 8.Body Diode Characteristics @ 25°C

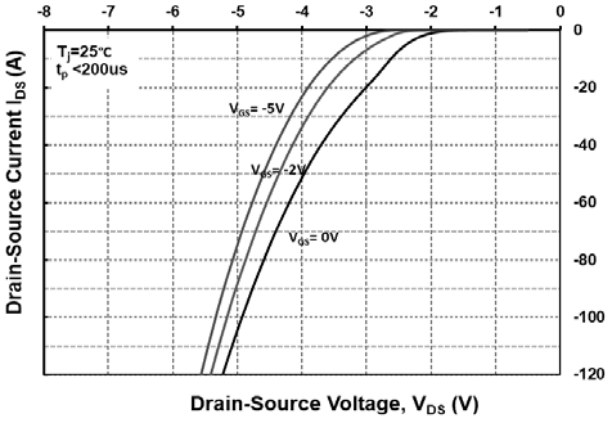


Fig 9. Body Diode Characterisics @ 150°C

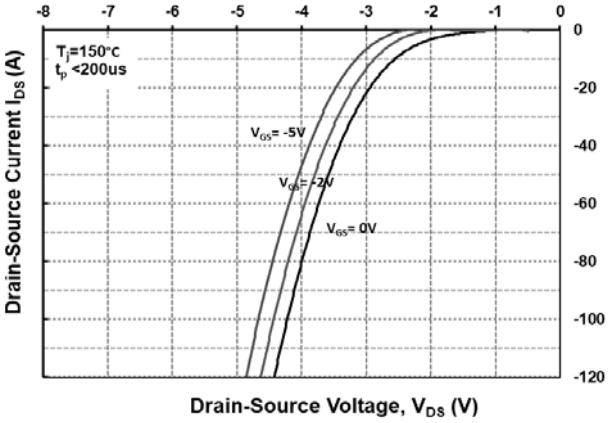


Fig 10. Body Diode Characteristics @ 175°C

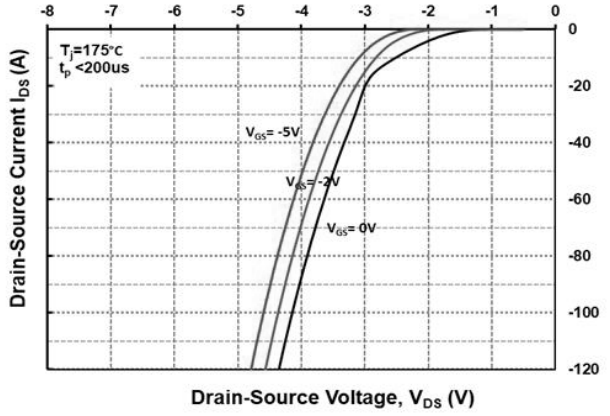


Fig 11. Threshold Voltage vs. Temperature

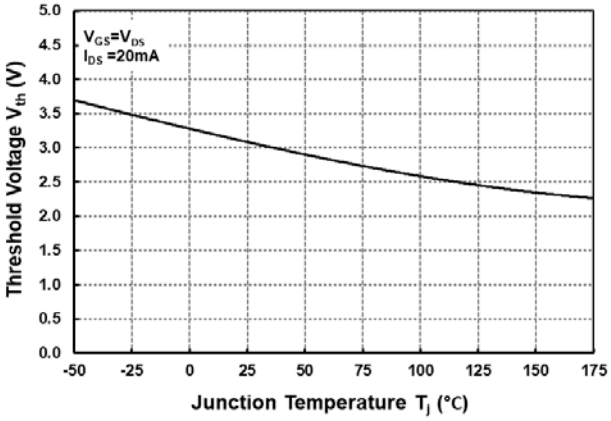
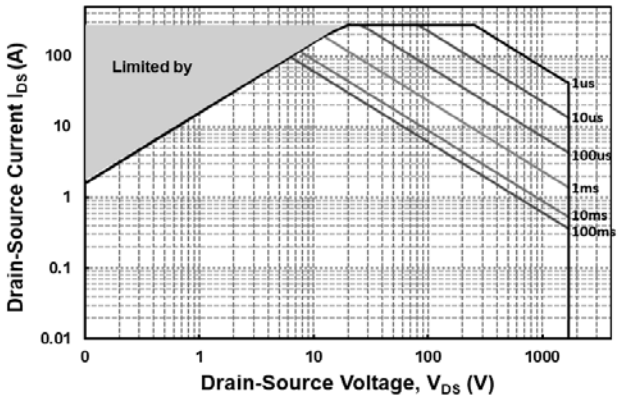


Fig 12. Safe Operating Area



Typical Performance

Fig 13. 3rd Quadrant Characteristics @ 25°C

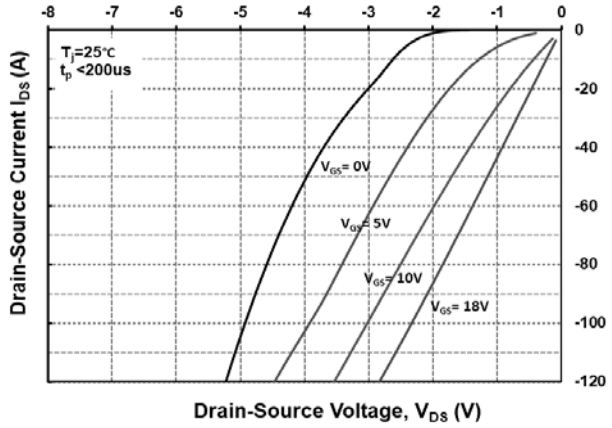


Fig 14. 3rd Quadrant Characteristics @ 150°C

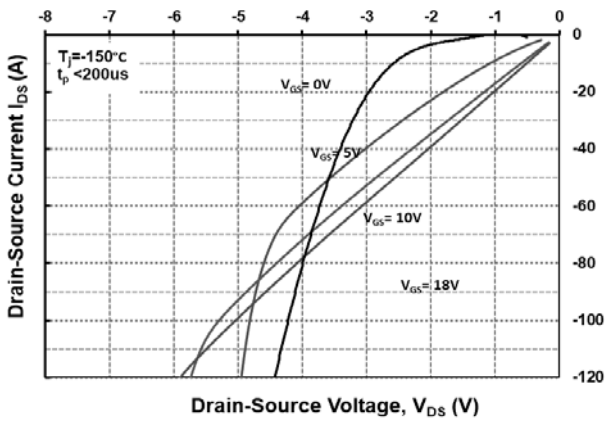


Fig 15. 3rd Quadrant Characteristics @ 175°C

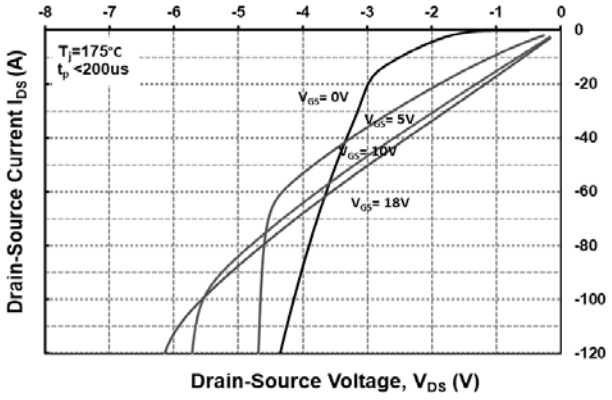


Fig 16. Output Capacitor Stored Energy

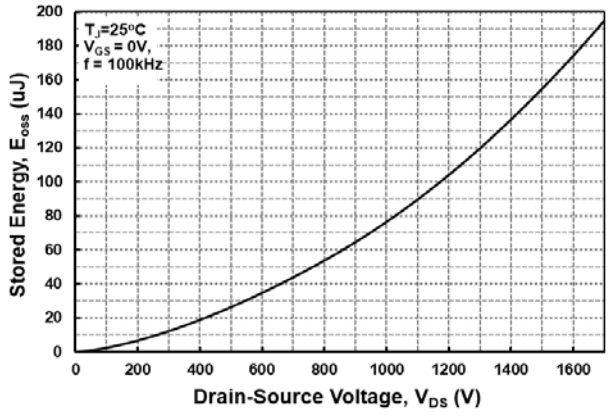


Fig 17. Capacitances vs. Drain-Source Voltage (0-200V)

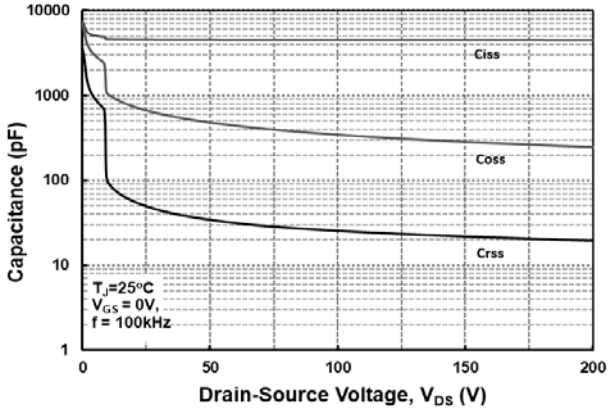
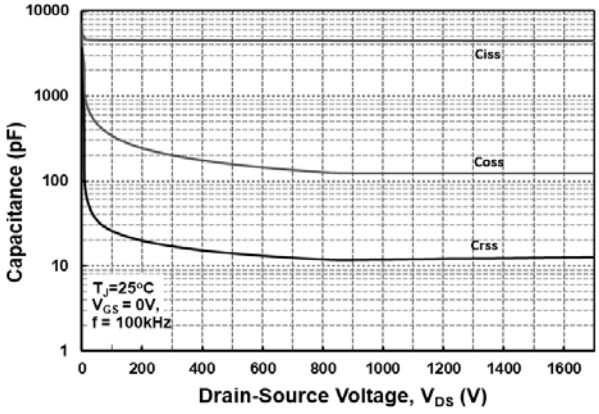


Fig 18. Capacitances vs. Drain-Source Voltage (0-1700V)



Typical Performance

Fig 19. Continuous Drain Current Derating vs. Case Temperature

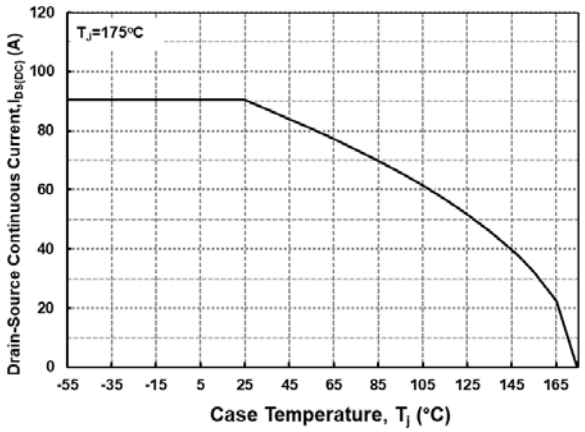


Fig 20. Maximum Power Dissipation Derating vs. Case Temperature

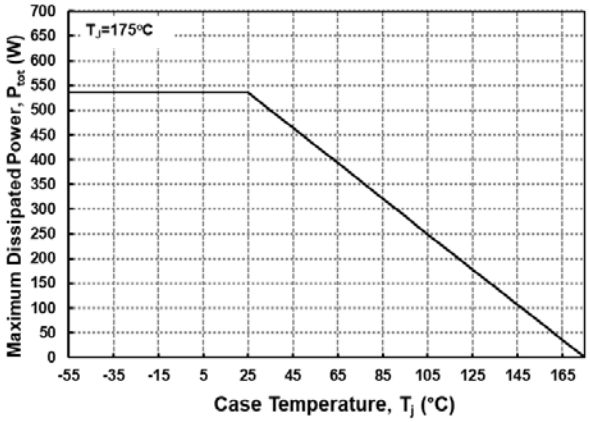


Fig 21. Clamped Inductive Switching Energy vs Drain Current (V_{DD} = 1200V)

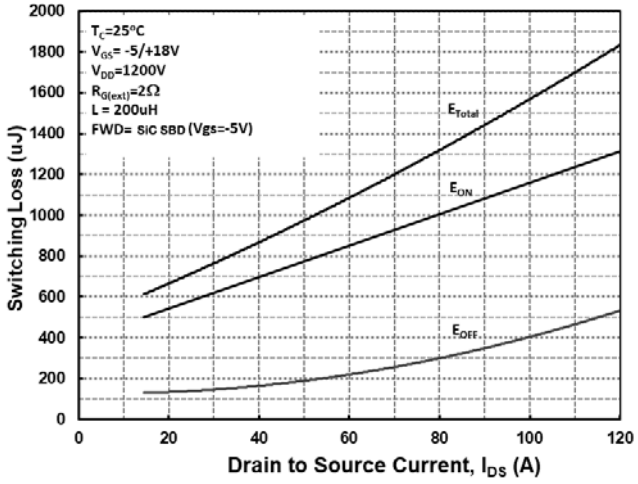


Fig 22. Clamped Inductive Switching Energy vs External Gate Resistor $R_{G(ext)}$

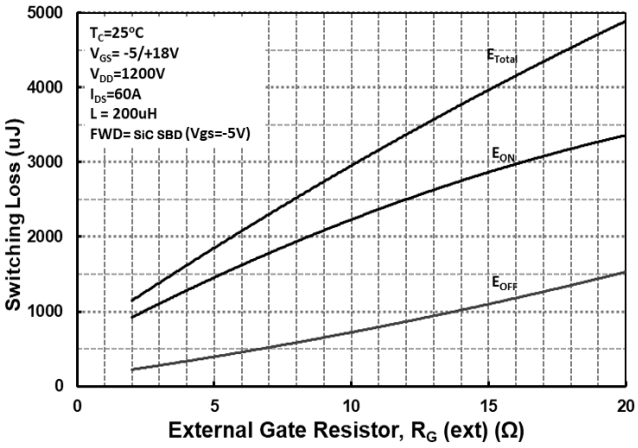


Fig 23. Switching Times vs Drain Current (V_{DD} = 1200V)

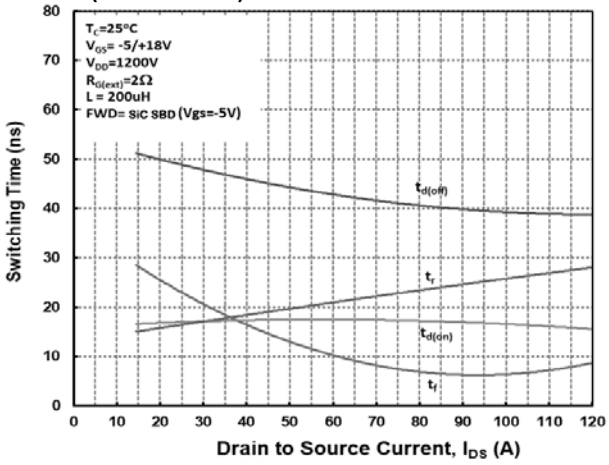


Fig 24. Switching Times vs External Gate Resistor $R_{G(ext)}$

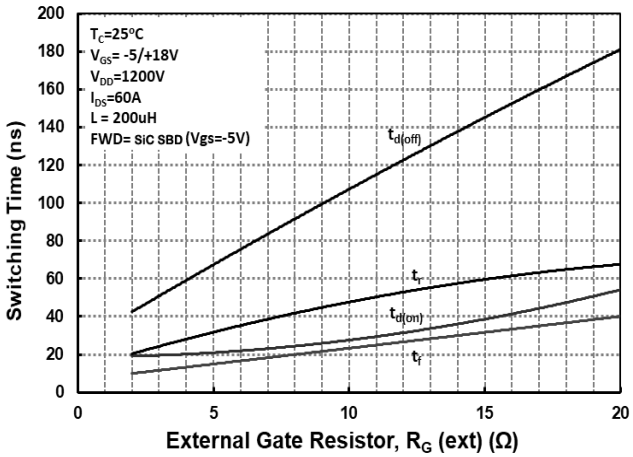
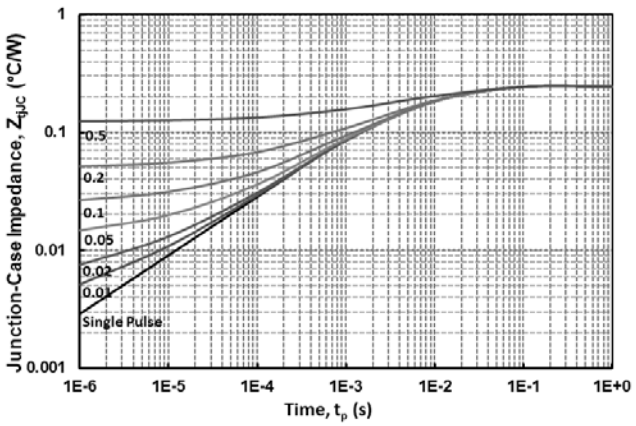


Fig 21. Transient Thermal Impedance (Junction – Case)

Fig 22. Safe Operating Area

Typical Performance

Fig 26. Transient Thermal Impedance (Junction – Case)



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