

DAC080N120ZY4

Silicon Carbide Enhancement Mode MOSFET

Features

- Low Capacitance With High Speed Switching Speed
- High frequency operation with low Capacitance
- Simple to drive with -5V/+18V gate
- Low Reverse Recovery (Qrr)
- Halogen Free and ROHS Compliant

Benefits

- Superior robustness and system reliability
- Simple to drive and easy to parallel
- Lower system cost of ownership
- Improved thermal capabilities and lower switching losses
- Faster and more efficient switching

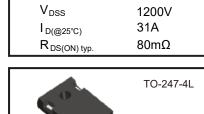
Applications

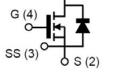
- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

Absolute Maximum Ratings

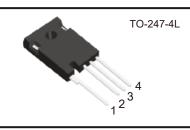
(Tc = 25°C unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage		V _{DS}	1200	v
Gate - Source Voltage (DC)	V _{GS}	-10/+22	v	
Recommended Operation Val	V _{GS(op)}	-5/+18	v	
Drain Current-Continuous	Tc=25°C Tc=100°C	ID	31 21	A
Pulse Drain Current	pulse width tp limited by T _{Jmax}	I _{DM}	83	A
Total Power Dissipation	PD	150	w	
Storage Temperature Range		T _{stg}	-55 to +175	°C
Operating Junction Temperature Range		TJ	-55 to +175	°C

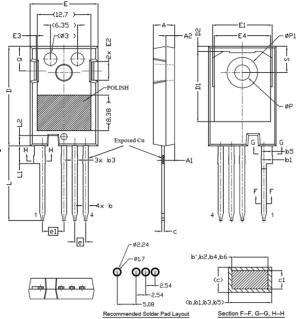




D (1)



Package Dimensions



					Unit : mm	
Symbol	Min	Max	Symbol	Min	Max	
А	4.83	5.21	D2	0.95	1.25	
A1	2.29	2.54	E	15.75	16.13	
A2	1.91	2.16	E1	13.10	14.15	
b'	1.07	1.28	E2	3.68	5.10	
b	1.07	1.33	E3	1.00	1.90	
b1	2.39	2.94	E4	12.38	13.43	
b2	2.39	2.84	е	2.54 BSC		
b3	1.07	1.60	e1	5.08 BSC		
b4	1.07	1.50	L	17.31	17.82	
b5	2.39	2.69	L1	3.97	4.37	
b6	2.39	2.64	L2	2.35	2.65	
с	0.55	0.68	ΦΡ	3.51	3.65	
c1	0.55	0.65	ΦP1	7.19 REF.		
D	23.30	23.60	Q	5.49	6.00	
D1	16.25	17.65	S	6.04	6.30	

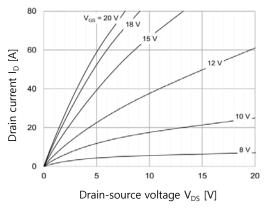


Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
OFF Characteristics							1
Drain-Source Breakdown Voltage	BVDSS	$V_{GS}=0V$, $I_{D}=1mA$		1200	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =1200V	T」=25℃	-	1	100	μA
		V _{GS} =0V	T」=150℃	-	5	-	
	lgss	$V_{GS}=22V$, $V_{DS}=0V$	1	-	-	100	
Gate-Source Leakage Current		V_{GS} =-10V , V_{DS} =0V		-	-	-100	nA
ON Characteristics						1	
Gate Threshold Voltage	VGS(th)	V_{DS} = V_{GS} , I_D =5mA		2.0	3.0	4.5	V
	RDS(on)	V _{GS} =18V,I _D =15A	T」=25℃	-	80	100	- mΩ
Drain-Source On-State Resistance			T」=175℃	-	128	-	
Internal Gate Resistance	RG(int.)	f =1MHz,V _{AC} =25mV	I	-	4	-	Ω.
Dynamic Characteristics							
Input Capacitance	Ciss			-	890	-	
Output Capacitance	Coss	- V _{DS} =800V V _{GS} =0V f =250kHz		-	65	-	pF
Reverse Transfer Capacitance	Crss			-	6	-	
Turn-On Switching Energy	Eon	V _{DS} =800V		-	77	-	
Turn-Off Switching Energy	Eoff	Vgs =-5/+18V		-	51	-	μJ
Total Switching Energy	Etot	I _D =15A R _{G(ext)} =2.0Ω		_	128	-	-
Switching Characteristics							
Turn-On Delay Time	td(on)			_	16	-	
Rise Time	tr	V _{DS} =800V V _{GS} =-5/+18V		-	11	-	-
Turn-Off Delay Time	td(off)	$V_{GS} = -5/+18V$ ID = 15A $R_{G(ext)} = 2.0\Omega$		-	23	-	ns
Fall Time	tf			-	9	-	
Total Gate Charge	Qg	VDS =800V VGS =-5/+18V ID =15A		-	53	-	nC
Gate to Source Charge	Qgs			-	15	-	
Gate to Drain Charge	Qgd			-	18	-	
Body Diode Characteristics						<u> </u>	1
Diode Forward Voltage	Vsd	Vgs=-5V,Isp=15A		-	4.1	-	V
Diode Source Current	ls			-	-	31	A
Maximum Diode Source Current (DC)	lsм			-	-	83	A
Reverse Recovery Time	Trr	Isd=15A,Vr=800V		-	15	-	n
Reverse Recovery Charge	Qrr	dif/dt=3000A/µs		-	125	-	n
Thermal Resistance				1		1	-
Thermal Resistance, Junction-to-Case	Rθյc			-	-	1.0	°C/



Typical Performance

Fig 1. Output Characteristics, T_J = 25°C





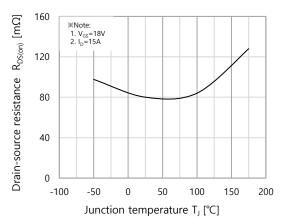


Fig 5. Threshold Voltage vs. Temperature

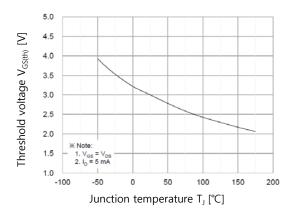


Fig 2. Output Characteristics, TJ = 175°C

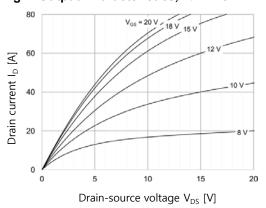


Fig 4. Transfer Characteristics

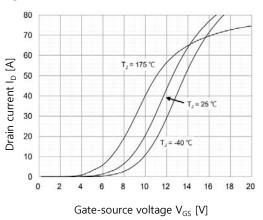
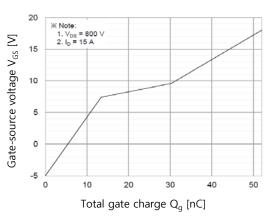


Fig 6. Gate Charge Characteristics





Typical Performance

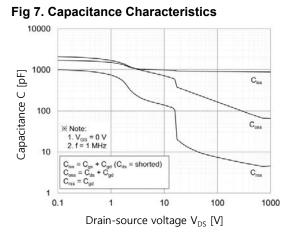


Fig 9. Body Diode Characteristics @ 25°C

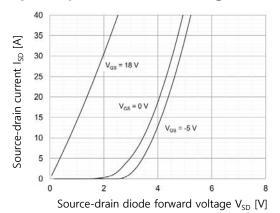
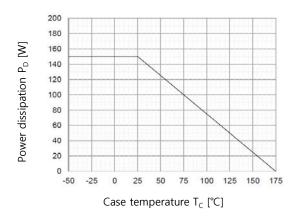


Fig 11. Max. PD Derating VS Case Temperature



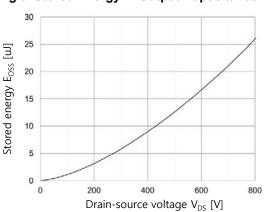
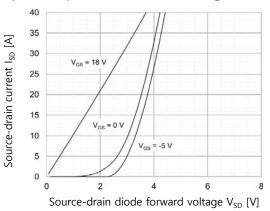
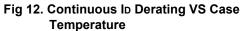


Fig 10. Body Diode Characteristics @ 175°C





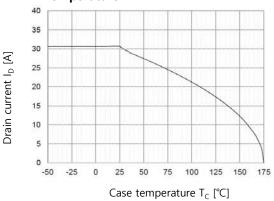


Fig 8. Stored Energy in Output Capacitance



Typical Performance

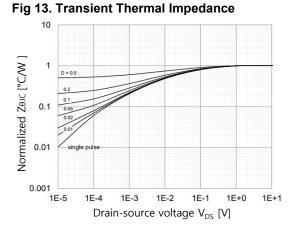


Fig 15. Clamped Inductive Switching Energy vs Drain Current

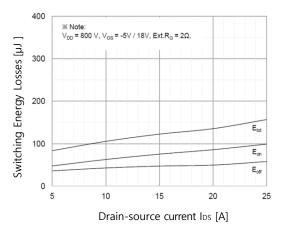
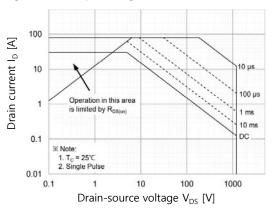
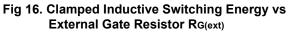
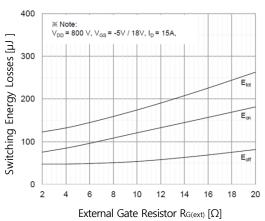


Fig 14. Safe Operating Area









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