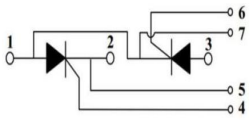
Thyristor Module

V_{RRM} / V_{DRM}	800 to 1600V
I_{TAV}	90 Amp
I_{TRMS}	140 Amp

TO-240AA



Features

- Aluminum oxide DBC
- Glass passivated chip
- Thyristor for line frequency
- Long-term stability

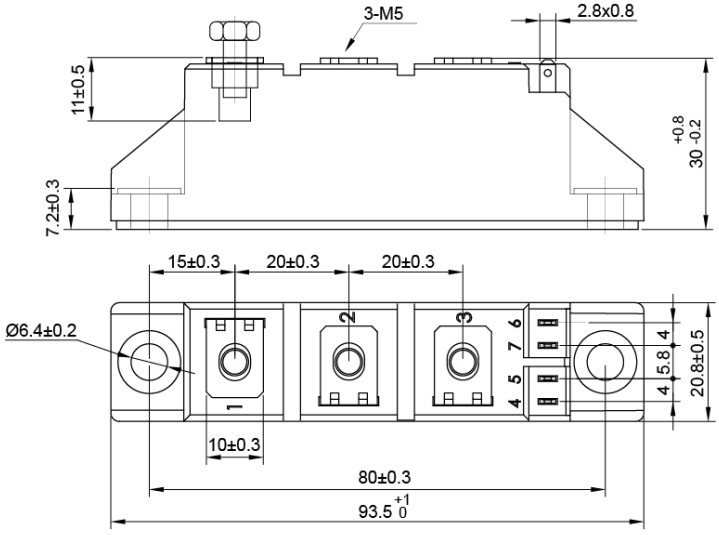
Applications

- Power converters
- Lighting control
- DC motor control and drives
- Heat and temperature control

Module Type

Type	V_{RRM} / V_{DRM}	V_{RSM}
DATTD090-08ST	800V	900V
DATTD090-12ST	1200V	1300V
DATTD090-16ST	1600V	1700V

Dimensions in mm



■ Maximum Ratings

Symbol	Item	Conditions	Values	Unit
I_{TAV}	Average On-state Current	180° Conduction Sin Half Wave, T_c = 85℃	90	A
I_{TRMS}	RMS On-state Current		140	A
I_{TSM}	Surge On-state Current	T_j = 25℃, t = 50Hz(10ms), V_R = 0V	2100	A
I^2t	Circuit Fusing Consideration	t = 10ms T_j =25℃	22050	A^2s
V_{ISO}	Isolation Breakdown Voltage	AC 50Hz/60Hz; R.M.S; 1min	3000	V
T_j	Operating Junction Temperature		-40 to + 125	℃
T_{stg}	Storage Temperature		-40 to + 125	℃
M_t	Mounting Torque	To Terminals(M5)	3±15%	N·m
M_s		To Heatsink(M6)	5±15%	
Weight	Module (Approximately)		105	g
di/dt	Critical Rate of Rise of On-state Current, Max	T_j= 125℃, V_D = 1/2V_{DRM}, I_G = 150mA, di_G/dt = 0.1A/μs	150	A/μs

■ Thermal Characteristics

Symbol	Item	Conditions	Values	Unit
$R_{th(j-c)}$	Thermal Impedance, Max	Junction to Case(Per Thyristor)	0.28	℃/W
$R_{th(c-s)}$	Thermal Impedance, Max	Case to Heat Sink	0.1	℃/W

■ Electrical Characteristics

Symbol	Item	Conditions	Values			Unit
			Min.	Typ.	Max.	
V_{TM}	Peak On-State Voltage, Max	T_j = 25℃, I_T = 270A	-	-	1.65	V
I_{DRM} /I_{RRM}	Repetitive Peak Reverse Current, Max /Repetitive Peak Off-state Current, Max	T_j = 125℃, V_R = V_{RRM}, V_D = V_{DRM}	-	-	20	mA
V_{GT}	Gate Trigger Voltage, Max	T_j = 25℃, V_D = 6V	-	-	3.0	V
I_{GT}	Gate Trigger Current, Max	T_j = 25℃, V_D = 6V	-	-	150	mA
V_{GD}	Gate Non-Trigger Voltage, Max	T_j = 125℃, V_D = 2/3V_{DRM}	-	-	0.25	V
I_L	Latching Current	T_j = 25℃	-	200	-	mA
I_H	Holding Current	T_j = 25℃	-	150	-	mA
t_{gt}	Turn On Time	T_j = 25℃	-	3	-	μs
dv/dt	Critical Rate of Rise of Off-state Voltage, Min	T_j = 125℃, V_D = 2/3V_{DRM} Linear Voltage Rise	1000			V/μs
V_{T0}	Threshold Voltage, for power loss calculation only	T_j = 125℃	0.85			V
r_T	Slope Resistance, for power loss calculation only	T_j = 125℃	2.8			mΩ

Performance Curves

Fig1. Gate Trigger Characteristics

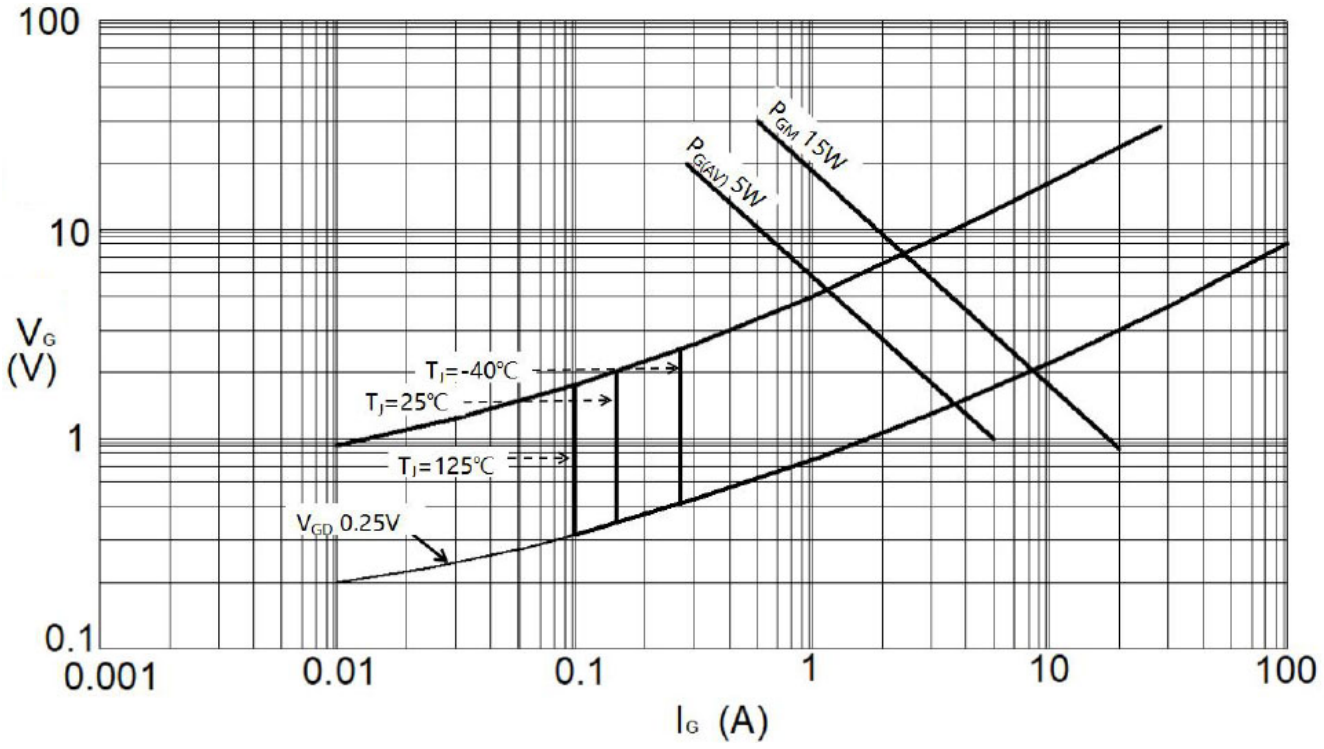


Fig2. Power Dissipation

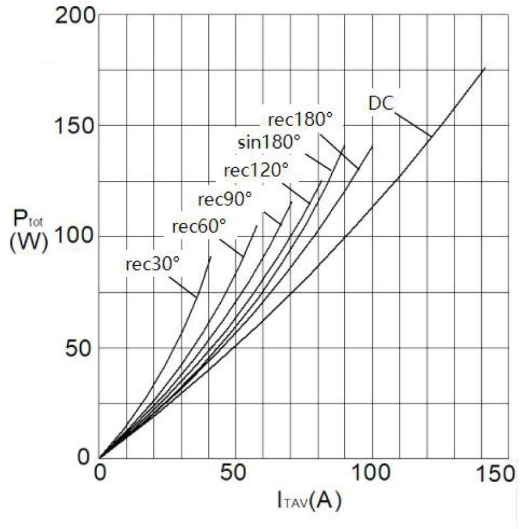


Fig3. Forward Current Derating Curve

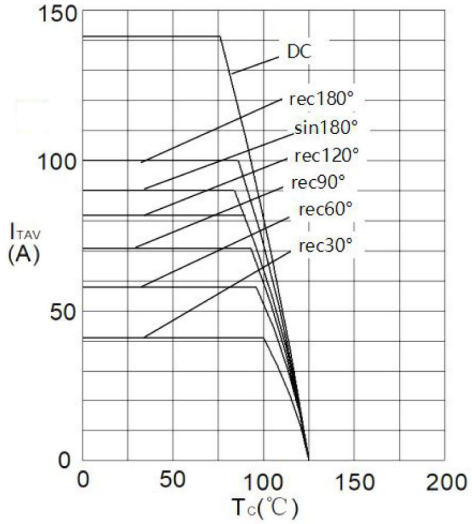


Fig4. Forward Characteristics

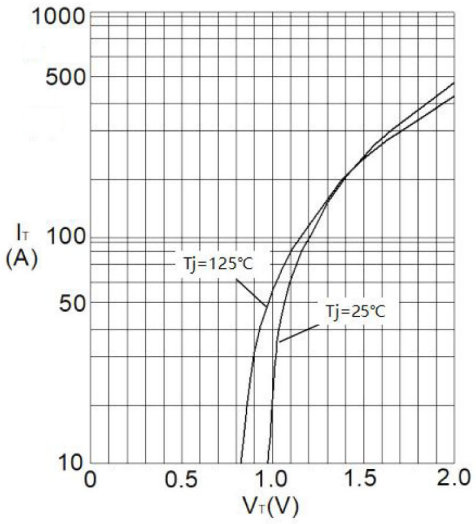


Fig5. Transient Thermal impedance

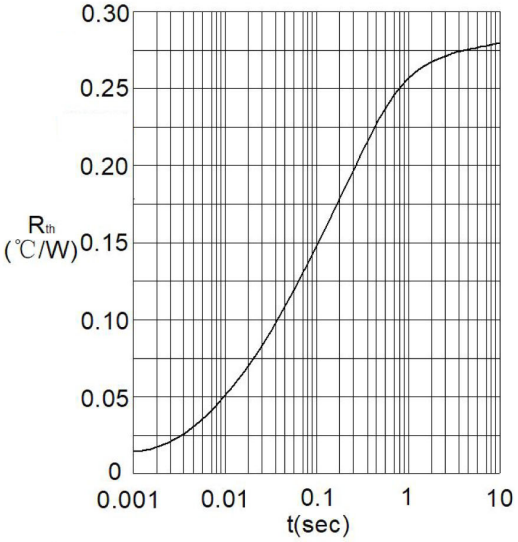
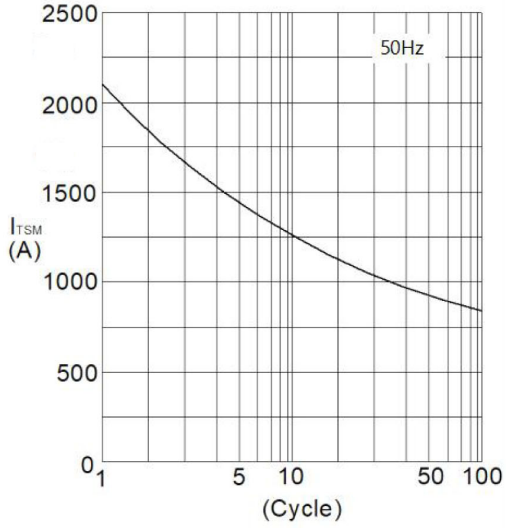


Fig6. Max Non-Repetitive Forward Surge Current



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