



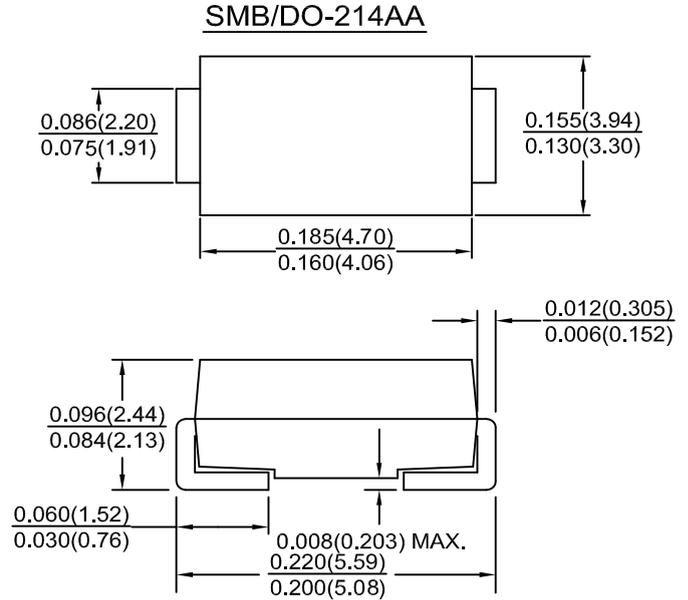
SURFACE MOUNT GLASS PASSIVATED JUNCTION RECTIFIER

FEATURES:

- For surface mounted applications
- Low profile package
- Built-in stain relief
- Easy pick and place
- Flammability Classification
- High temperature soldering:
250°C /10 second at terminals

MECHANICAL DATA

Case: JEDEC DO-214AA molded plastic
 Terminals: Solder plated solderable per MIL-STD-750, Method 2026
 Polarity: Indicated by cathode band
 Standard Packaging: Any
 Weight: 0.003 ounces, 0.093 grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25° C ambient temp. unless otherwise specified.
 Single phase, half sine wave, 60 Hz, resistive or inductive load.
 For capacitive load, derate current by 20 %.

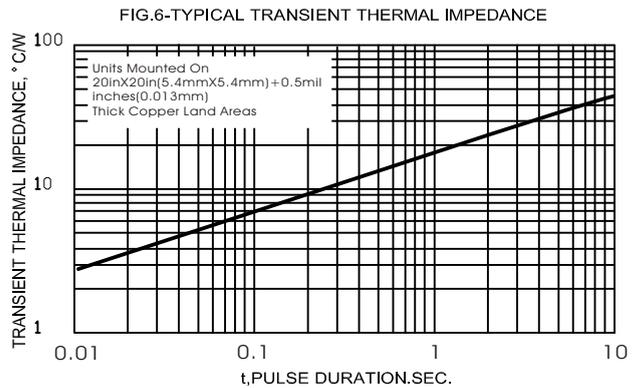
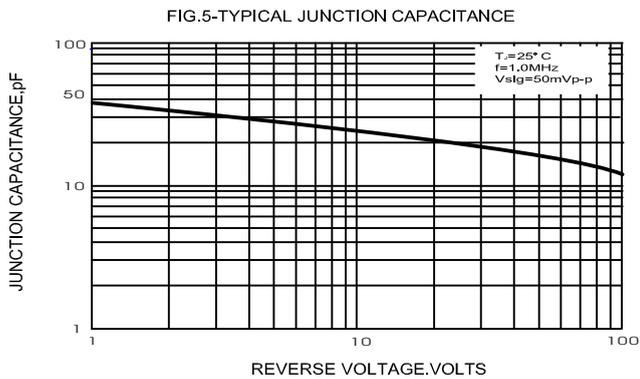
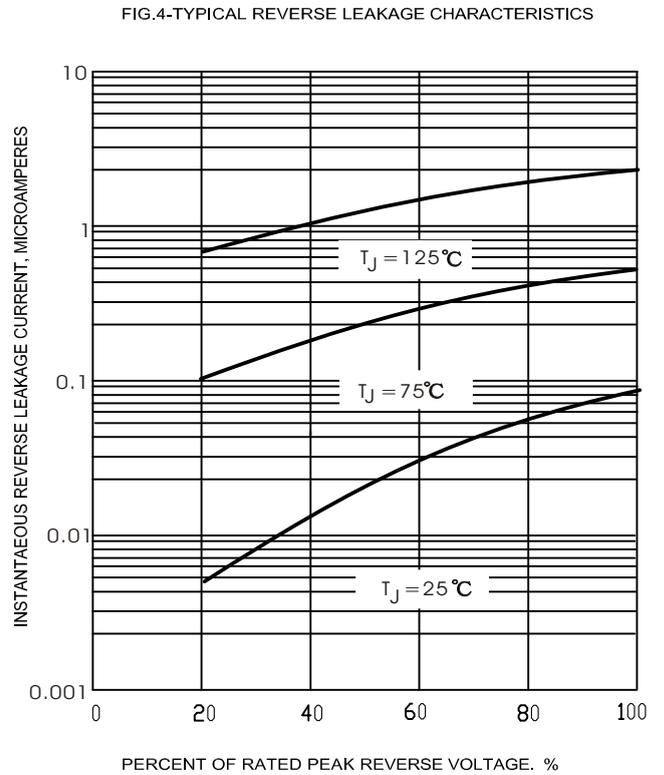
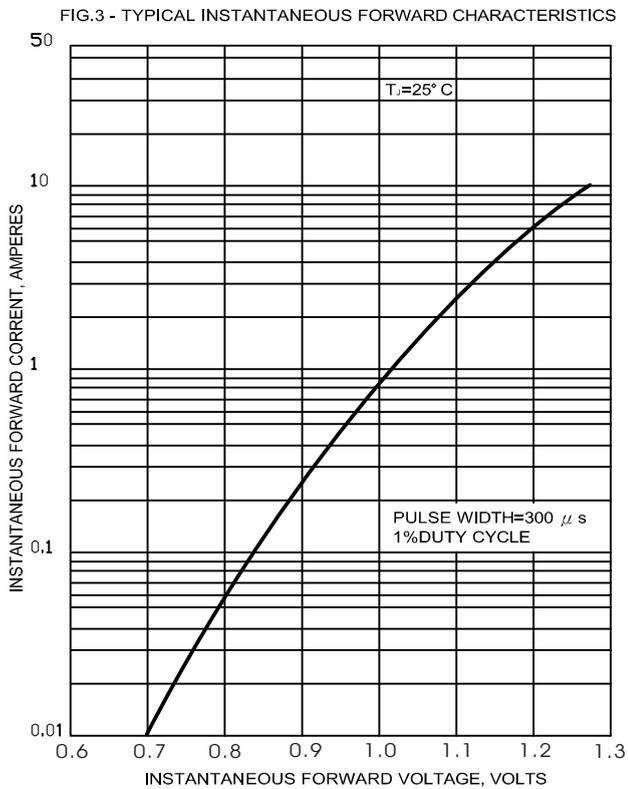
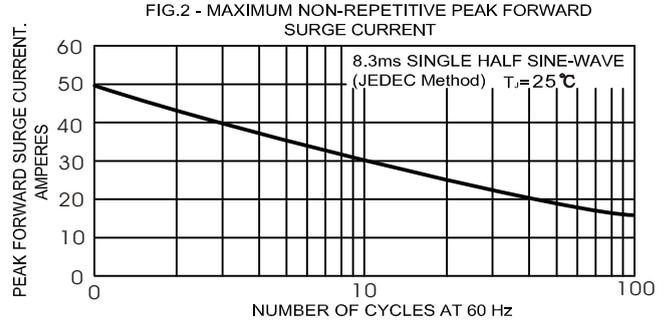
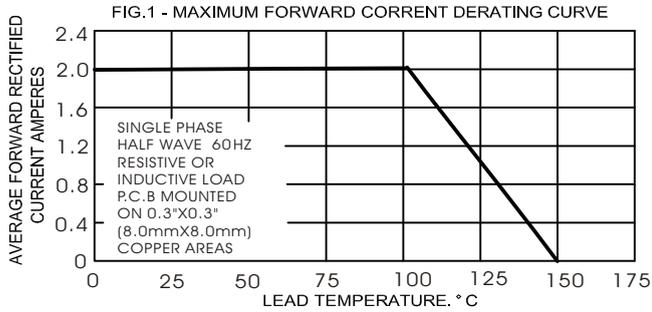
Characteristic	Symbol	S2A	S2B	S2D	S2G	S2J	S2K	S2M	Units
Maximum recurrent peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	Volts
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	Volts
Maximum DC voltage	V _{DC}	50	100	200	400	600	800	1000	Volts
Maximum average forward rectified current at T _L = 100 °C	I _o	2.0							Amps
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	50							Amps
Maximum instantaneous forward voltage drop per leg at 2.0A	V _F	1.1							Volts
Maximum DC reverse current at rated DC blocking voltage T _a = 25 °C T _a = 125 °C	I _R	5.0 125							μ A
Typical Junction Capacitance (NOTE 2)	C _J	30							PF
Maximum reverse recovery time (NOTE 1)	TRR	2.5							μs
Typical thermal resistance (NOTE 3)	R _{th JL}	16							°C/W
Operating Junction and storage temperature range	T _J , T _{stg}	-55 to +150							°C

NOTE :

1. Reverse recovery test conditions: I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A
2. Measured at 1 MHz and applied reverse voltage of 4.0 volts
3. Thermal resistance from junction to lead mounted on 0.2 x 0.2" (5.0mm x 5.0mm)



RATINGS AND CHARACTERISTIC CURVES





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