

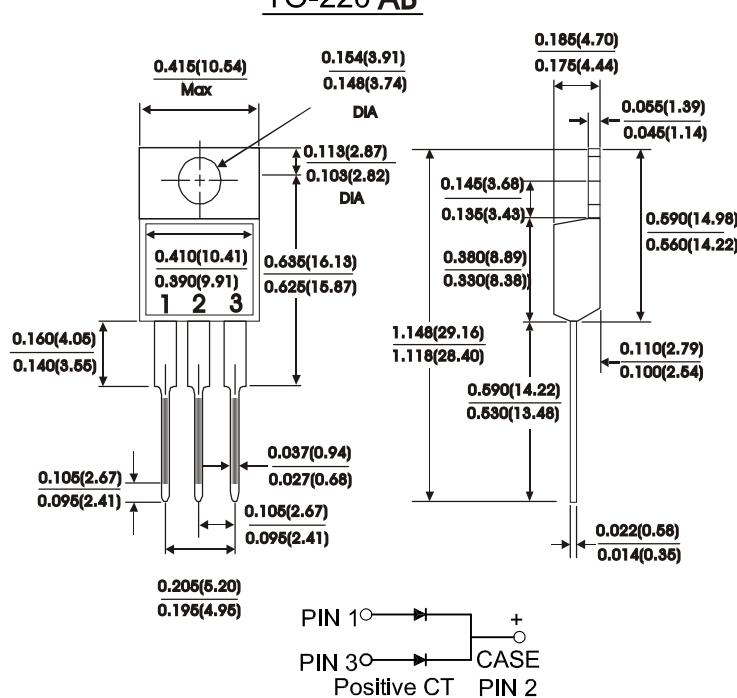
ULTRA FAST GLASS PASSIVATED RECTIFIERS

FEATURES:

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Ideally suited for free wheeling diode power factor correction applications
- Excellent high temperature switching
- Optimized to reduce switching losses
- High temperature soldering guaranteed: 250°C/10 seconds, 0.25"(6.35mm) from case

MECHANICAL DATA

Case : JEDEC TO-220AB molded plastic
 Terminals : Leads solderable per MIL-STD-750
 Method 2026
 Polarity : As marked
 Mounting Position : Any
 Mounting Torque 5 In - lbs.max
 Weight : 0.08 ounce, 2.24 grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase half wave, 60 Hz resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	UF 20005CT	UF 2001CT	UF 2002CT	UF 2003CT	UF 2004CT	UF 2006CT	UF 2008CT	Units
Maximum recurrent peak reverse voltage	V _{RRM}	50	100	200	300	400	600	800	Volts
Maximum RMS voltage	V _{RMS}	35	70	140	210	280	280	560	Volts
Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	600	800	Volts
Maximum average forward rectified current at T _c =100°C	I _O				20.0				Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}				150				Amps
Maximum instantaneous forward voltage (Per leg)	V _F		1.0		1.30		1.70		Volts
Maximum DC reverse current (Per leg) T _c =25 °C at rated DC blocking voltage T _c =125°C	I _R			10.0	500.0				μA
Typical reverse recovery time (NOTE 1)(Per leg)	T _{RR}	50		75		100			nS
Typical junction capacitance (NOTE 2)(Per leg)	C _J		50						P _F
Operating and storage temperature range	T _J		-55 to +150						°C
Storage temperature range	T _{Stg}		-55 to +150						°C

NOTES:

(1)Reverse Recovery Test CONDITION : I_F=0.5A, I_R=1.0A, I_{RR}=0.25A

(2)Measured at 1MHZ and reverse Voltage of 4.0V

(3)Marking : UF20005CT = UF20005 (Without Marking "CT")
 Symbol Marking

RATINGS AND CHARACTERISTIC CURVES UF20005CT THRU UF2008CT

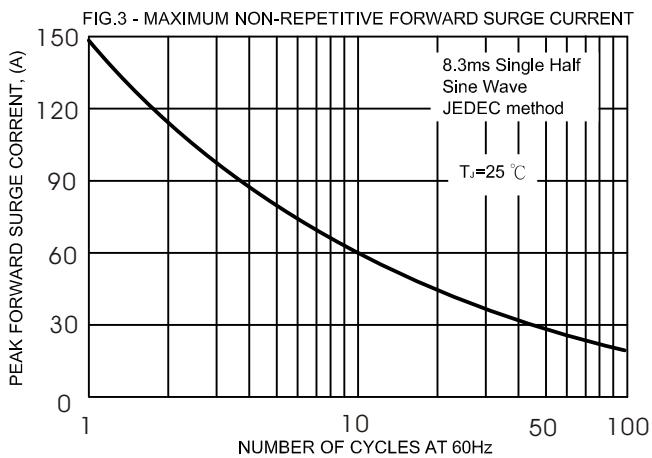
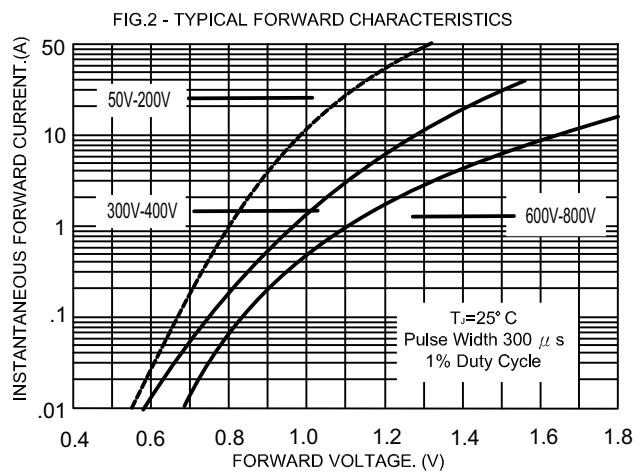
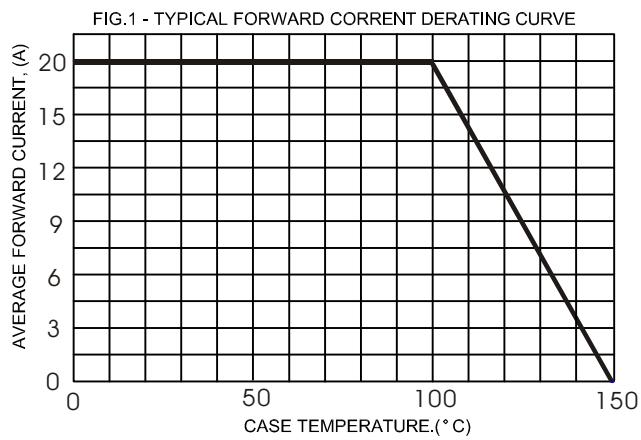


Figure 6 GR1 Test Circuit

